

DESCRIPTION

WIRELESS SIGNAL SWITCHING CIRCUIT AND WIRELESS
5 COMMUNICATION APPARATUS

TECHNICAL FIELD

The present invention relates to a wireless signal
switching circuit for switching signals when transmitting
10 a plurality of wireless transmitter signals or receiving
a plurality of wireless receiver signals by using
antennas and a wireless communication apparatus using the
same.

The wireless signal switching circuit of the present
15 invention is applied to a multi-band compatible mobile
phone (cellular phone) or other mobile wireless
communication apparatus etc.

BACKGROUND ART

In recent years, in mobile wireless communication
20 terminals such as mobile phones or PDA's (personal
digital assistants), lower power consumption, smaller
size, and lighter weight have been realized. Realization
of multi-band and multi-mode functions is inevitable.
Along with this, such multi-band compatible mobile
25 wireless communication terminals are being provided with
wireless signal switching circuits for switching a

plurality of wireless transmitter signals or a plurality of wireless receiver signals. In such wireless signal switching circuits, reduction of loss at the time of switching among a plurality of routes and in routes has
5 been strongly demanded.

Referring to the document "Chip Multilayer Antenna Switch Module for Triple Band Phone (EGSM/DCS/PCS)", see HITACHI METALS, URL: <http://www.hitachi-metals.co.jp/product/isc2001/asm/shs1090t.pdf>, an example
10 of signal switching in a wireless signal switching circuit used in a mobile phone (cellular phone) or other mobile wireless communication terminal will be explained. For example, a case of using three frequencies F1, F2 and F3 as the multi-bands will be explained. However, assume
15 that the frequencies have a relationship of $F1 \ll F2 < F3$. For example, $F1 = 900 \text{ MHz}$, $F2 = 1800 \text{ MHz}$, and $F3 = 1900 \text{ MHz}$.

In the wireless signal switching circuit illustrated in FIG. 1, a diplexer 102 directly connected to an
20 antenna terminal 101 of a wireless transmitter and receiver antenna ANT roughly divides a frequency domain of the transmitter and receiver signal into two. Namely, the diplexer 102 divides the frequency domain into a frequency F1 having a low frequency and frequencies F2
25 and F3 having a high frequency.

A switch element 103 separates a transmitter signal

F1TX and a receiver signal F1RX of the frequency F1. The transmitter signal F1TX of the frequency F1 is applied to the switch element 103 through a low pass filter 106 and transmitted from the wireless transmitter and receiver
5 antenna ANT through the diplexer 102. The receiver signal F1RX of the frequency F1 received at the wireless transmitter and receiver antenna ANT is divided in frequency at the diplexer 102 and output from the switch element 103.

10 The switch element 104 separates the transmitter signal F2 or F3TX of the frequency F2 or F3 (F2/F3), and the receiver signal F2RX of the frequency F2 and the receiver signal F3RX of the frequency F3. The transmitter signal F2 or F3TX of the frequency F2 or F3 is applied to
15 the switch element 104 through the low pass filter 107 and transmitted from the wireless transmitter and receiver antenna ANT through the diplexer 102. The receiver signal F2 or F3RX of the frequency F2 or F3 received at the wireless transmitter and receiver antenna
20 ANT is separated at the diplexer 102 and output from the switch element 104. Further, the switch element 105 separates the receiver signal F2RX of the frequency F2 and the receiver signal F3RX of the frequency F3.

The wireless signal switching circuit explained
25 above employs a configuration roughly dividing the frequency domain into two at first by using the diplexer

102, then switching routes of frequencies by using switch
elements 103 to 105, therefore requires many switch
elements. For example, at the reception side of the
frequency F2 or F3, signals pass through the diplexer 102,
5 the switch element 104 and the switch element 105, so the
losses at the switch elements accumulate resulting in a
larger attenuation of the receiver signal. The
attenuation of the receiver signal causes the
disadvantages of the necessity of adding a signal
10 amplifier circuit and susceptibility to noise.

In the example explained above, there were three
types of frequency, but the larger the type of the
frequency to be used, the larger the number of switches
for switching the routes accompanied with that. When a
15 PIN (positive intrinsic negative) diode, for example, as
such a switch element is used, the power consumption
becomes large.

When using such a wireless signal switching circuit
in a mobile phone or other wireless communication
20 apparatus, the power consumption of the wireless
communication apparatus becomes large. For example, a
mobile phone is driven by a battery, therefore the
increase of the power consumption causes the disadvantage
such as shortening of the service life of the battery.

25 DISCLOSURE OF THE INVENTION

An object of the present invention is to provide a

wireless signal switching circuit for reducing the loss and reducing the power consumption.

Another object of the present invention is to provide a wireless communication apparatus using the
5 wireless signal switching circuit for reducing the loss and reducing the power consumption.

According to a first aspect of the present invention, there is provided a wireless signal switching circuit for switching a plurality of transmitter signals and receiver
10 signals having different frequencies in wireless communication for communication by at least a first communication system and a second communication system, including an antenna terminal connected to an antenna; a first signal route switching means having a plurality of
15 switch means for selecting a plurality of transmitter signals and receiver signals having different frequencies in the first communication system; a phase rotating means having one end connected to the antenna terminal and imparting a phase rotation of 90 degrees to the phase of
20 the signal of the frequency component supplied to the first signal route switching means; and a second signal route switching means having a diplexer for separating the transmitter signals and receiver signals having a further different frequency of the first communication
25 system lower than the plurality of frequencies in the first communication system explained above and the

transmitter and receiver signals of the second communication system, a common input and output terminal of the diplexer being connected to the other end of the phase rotating means, a first filter side terminal of the diplexer being supplied with transmitter and receiver signals having a further different frequency of the first communication system, and a second filter side terminal of the diplexer being supplied with transmitter and receiver signals of the second communication system.

10 Preferably, the phase rotating means has a characteristic of attenuating harmonic components of signals transmitted by the second communication system.

Further preferably, the first filter side of the diplexer is a low frequency filter side, and the second filter side of the diplexer is a high frequency filter side.

15 Preferably, the phase rotating means is provided with an inductor having one end connected to the antenna terminal and having the other end connected to the common input and output terminal of the diplexer, a first capacitor connected between one end of the inductor and a reference potential node, a second capacitor connected between the other end of the inductor and the reference potential node, a first switch means having one end connected to the other end of the inductor, and a third capacitor connected between the other end of the first

switch means and the reference potential node, and has the characteristics that a circuit defined by the inductor and the first to third capacitors imparts a phase rotation of 90 degrees to the phase of the signal of the frequency component supplied to the first signal route switching means when the first switch means is activated and that a circuit defined by the inductor and the first and second capacitors attenuates the harmonic component of the signal transmitted by the second communication system when the first switch means is deactivated.

For example, the first communication system is a triple band GSM system, and the second communication system is a UMTS system.

Preferably, the first signal route switching means is provided with a first receiver signal switching circuit connected to the antenna terminal and having a plurality of switch means for selecting receiver signals having a plurality of different frequencies in the first communication system and a first transmitter signal switching circuit connected to the antenna terminal and having a switch means for selecting transmitter signals having a plurality of different frequencies in the first communication system and a filter means connected to the switch means.

More preferably, the second signal route switching

means has a switch means connected to the first filter
side terminal of the diplexer and selecting a transmitter
signal having a further different frequency of the first
communication system and a switch means for selecting a
5 receiver signal having a further different frequency of
the first communication system.

According to a second aspect of the present
invention, there is provided a wireless communication
apparatus provided with a wireless transmitter and
10 receiver antenna and a wireless signal switching circuit.

Preferably, the wireless communication apparatus is
a mobile wireless communication apparatus including a
dual mode compatible mobile phone of a triple band GSM
system as the first communication system and a UMTS
15 system as the second communication system.

More preferably, the second filter side terminal of
the diplexer is connected to a front end of a UMTS use
transmission and reception circuit, and the front end has
a diplexer for switching the UMTS transmitter signal and
20 UMTS receiver signal, a low noise amplifier circuit for
amplifying the UMTS receiver signal input through this
duplexer, and a power amplifier circuit for amplifying
the UMTS transmitter signal.

These and other objects and features of the present
25 invention of the present invention will become clearer
from the following description given with reference to

the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a circuit diagram of a conventional wireless signal switching circuit.

5 FIG. 2 is a circuit diagram of a wireless signal switching circuit of an embodiment of the wireless signal switching circuit of the present invention.

FIGS. 3A and 3B are waveform diagrams of an input signal and output signal of a 90 degree phase rotation
10 circuit in the wireless signal switching circuit illustrated in FIG. 2.

FIG. 4 is a diagram showing an attenuation characteristic of the 90 degree phase rotation circuit in the wireless signal switching circuit illustrated in FIG.
15 2.

FIG. 5 is a partial diagram of a dual mode compatible mobile phone using the wireless signal switching circuit illustrated in FIG. 2 as an embodiment of the wireless communication apparatus of the present
20 invention.

BEST MODE FOR CARRYING OUT THE INVENTION

Below, a detailed explanation will be given of preferred embodiments of the wireless signal switching circuit and the wireless communication apparatus of the
25 present invention with reference to the drawings.

First Embodiment

FIG. 2 is a circuit diagram of an example of the configuration of a wireless signal switching circuit as an embodiment of the wireless signal switching circuit of the present invention.

5 In the present embodiment, a dual mode compatible mobile phone (cellular phone) having for example a triple band GSM (global system for mobile communication system) system as the first communication system and for example a UMTS (universal mobile telecommunications system)
10 system as the second communication system is exemplified.

Further, a TDMA (time division multiple access) operation using three frequencies F1, F2 and F3, i.e., three frequencies having a relationship of $F1 \ll F2 < F3$, as the multi-bands of the GSM system is carried out. In
15 the dual mode in the UMTS system, a GSM and UMTS dual mode performing a FDD (frequency division duplex) operation is exemplified.

As an example, $F1 = 900 \text{ MHz}$, $F2 = 1800 \text{ MHz}$ and $F3 = 1900 \text{ MHz}$ are set. Further, in the UMTS system, the
20 transmission and reception are simultaneously carried out. As an example, the transmission frequency is set to 1950 MHz, and the reception frequency is set to 2150 MHz.

The wireless signal switching circuit 1 illustrated in FIG. 2 has an antenna terminal 11 connected to the
25 wireless transmitter and receiver antenna ANT, a transmission terminal 12 connected to each transmission

system (TX) of frequency F2 or F3, a transmission
terminal 13 connected to the transmission system (TX) of
the frequency F1, a reception terminal 14 connected to
the reception system (RX) of the frequency F1, reception
5 terminals 15 and 16 connected to reception systems of
frequencies F2 and F3, and a transmission and reception
terminal 17 connected to the UMTS transmission and
reception system. Further, the wireless signal switching
circuit 1 is provided with, provided between these
10 terminals, a signal route switch 20 of an F2 or F3
transmission and reception system, a signal route
switching circuit 30 of an F1/UMTS transmission and
reception system, and a 90 degree phase rotation circuit
40.

15 Especially, the 90 degree phase rotation circuit 40
is provided between the signal route switch 20 of the F2
or F3 transmission and reception system and the signal
route switching circuit 30 of the F1/UMTS transmission
and reception system.

20 The wireless signal switching circuit 1 is further
provided with a controlling means 60 for controlling
opening/closing of switch elements in the signal route
switch 20 of the F2/F3 transmission and reception system,
the signal route switching circuit 30 of the F1/UMTS
25 transmission and reception system, and the 90 degree
phase rotation circuit 40.

In the present Description, "/" means "or". For example, F2/F3 means F2 or F3. On the other hand, "." means "and". For example, F1/UMTS means F1 and UMTS.

The route switching circuit 20 of the F2 or F3
5 transmission and reception system of an example of the first signal route switching means of the present invention has a switch element 21 connected in series between the antenna terminal 11 and the transmission terminal 12 for the F2 or F3 transmitter signal F2 or
10 F3TX and a harmonic suppressor filter, for example, a low pass filter (LPF) 22. Further, the signal route switch 20 of the F2 or F3 transmission and reception system has a switch element 23 connected between the antenna terminal 11 and the reception terminal 15 in order to select and
15 extract the receiver signal F2RX of the frequency F2. Further, the signal route switch 20 of the F2 or F3 transmission and reception system has a switch element 24 connected between the antenna terminal 11 and the reception terminal 16 in order to select and extract the
20 receiver signal F3RX of the frequency F3.

The signal route switching circuit 30 of the F1 and UMTS transmission and reception system of an example of the second signal route switching means of the present invention has a diplexer 31 comprised of a combination of
25 pass filters of bands corresponding to the multi-mode frequencies, i.e., the low pass filter (LPF) and high

pass filter (HPF) in the present example, and having the HPF side terminal connected to the UMTS transmission and reception terminal 17, a switch element 32 connected between the LPF side terminal of this diplexer 31 and the F1 transmitter signal (F1TX) use terminal 13, and a switch element 33 connected between the LPF side terminal of the diplexer 31 and the F1 receiver signal (F1RX) use terminal 14. In the signal route switching circuit 30 of the F1 and UMTS transmission and reception system, the HPF side terminal of the diplexer 31 is connected to the UMTS transmission and reception terminal 17.

The 90 degree phase rotation circuit 40 has an inductor 41 having one end 40a connected to the antenna terminal 11 and having the other end 40b connected to an input and output terminal 31a of the diplexer 31, a capacitor 42 connected between one end side 40a of the inductor 41 and the ground as the reference potential node, a capacitor 43 connected between the other end 40b of the inductor 41 and the ground, and a switch element 44 and capacitor 45 connected in series between the other end 40b of the inductor 41 and the ground.

In the 90 degree phase rotation circuit 40, the inductor 41, capacitor 42 and the capacitor 43 are always connected, but the capacitor 45 is connected or disconnected to the inductor 41, the capacitor 42 and the capacitor 43 in accordance with the opening/closing of

the switch element 44 on the basis of the controlling means 60.

The 90 degree phase rotation circuit 40 has a characteristic passing the transmitter signal or receiver signal having the frequency F1 or the UMTS transmitter and receiver signal therethrough, while rotating the phase by 90 degrees and isolating the transmitter signal or the receiver signal having the frequency F2 or F3 so that the signal of the frequency F2 or F3 is substantially not applied to the signal route switching circuit 30 of the F1 and UMTS transmission and reception system.

For this reason, in the 90 degree phase rotation circuit 40, an inductance L of the inductor 41 and capacitances C1 and C2 of the capacitors 42 and 43 rotate the phases of signals of frequencies F2 and F3 by 90 degrees. Preferably, the 90 degree phase rotation circuit 40 is set to a value so that it further has the characteristics of a low pass filter (LPF) exhibiting an attenuation characteristic with respect to the harmonic components (frequency components 2 times or 3 times the transmission frequency) of the transmission frequency used in the UMTS route from the UMTS transmission and reception terminal 17 as illustrated in FIG. 4.

FIG. 3A and FIG. 3B show the input and output characteristics of the 90 degree phase rotation circuit

40. When the sine wave input signal illustrated in FIG.
3A is applied to the 90 degree phase rotation circuit 40,
the output thereof becomes an output signal with a phase
delay of 90 degrees illustrated in FIG. 3B. In FIG. 3A
5 and FIG. 3B, the abscissas show the time.

FIG. 4 shows an example of the attenuation
characteristic of the 90 degree phase rotation circuit 40.
In the present example, $F1 = 900$ MHz, $F2 = 1800$ MHz, and
 $F3 = 1900$ MHz. Further, the transmission frequency of the
10 UMTS system is set to 1950 MHz, and the reception
frequency is set to 2150 MHz.

The attenuation characteristic illustrated in FIG. 4
is set to a value that also has the characteristic of a
low pass filter (LPF) exhibiting an attenuation
15 characteristic of almost no attenuation at $F1 = 900$ MHz,
but large attenuation with respect to the harmonic
components (frequency components 2 times or 3 times the
transmission frequency 1950 MHz) of the transmission
frequency used in the UMTS route from the UMTS
20 transmission and reception terminal 17.

The switch element 44 is driven by the controlling
means 60 so as to open or close in synchronization with
the opening or closing operation of the switch element 21
which becomes "closed" in order to apply the $F2$ or $F3$
25 transmitter signal $F2$ or $F3TX$ to the antenna terminal 11
and the switch element 23 for selecting the $F2$ receiver

signal F2RX or the switch element 24 for selecting the F3 receiver signal F3RX.

As the switch elements 21, 23 and 24 in the signal route switch 20 of the F2 or F3 transmission and reception system, the switch elements 32 and 33 in the signal route switching circuit 30 of the F1 or UMTS transmission and reception system, and the switch elements 42, 43 and 44 in the 90 degree phase rotation circuit 40, use can be made of semiconductor switches such as field effect transistors (FET) or PIN diodes. These switch elements are preferably elements having good high frequency characteristics, that is, able to perform high speed operation, and having little leakage current.

These switch elements are opened or closed in accordance with a control instruction of the controlling means 60. In the present Description, for example, the switch element 21 and the controlling means 60 are together referred to as the "switch means".

In the present Description, for example, setting the switch element 21 to the closed state (or on state) is referred to as the "activated state", while setting the switch element 21 to the opening state (or off state) is referred to as the "de-activated state".

The operation of the wireless signal switching circuit 1 having the above configuration will be explained next.

(1) Time when transmitting transmitter signal F2 or F3TX using frequency F2 or F3

The controlling means 60 sets the switch element 21 to the closed (on) state and sets the switch element 23 and 24 to the opening (off) state, then sets the switch element 44 to the closed state. Naturally, the controlling means 60 set the switch element 32 and the switch element 33 to the opening state.

In this state, the transmitter signal F2 or F3TX applied to the transmission terminal 12 passes through the LPF 22 and the switch element 21, is applied to the antenna terminal 11, and is transmitted from the wireless transmitter and receiver antenna ANT.

When the controlling means 60 sets the switch element 44 to the closed state, the circuit configured by the inductor 41 and the capacitors 42, 43 and 45 exhibits a characteristic rotating the phase by 90 degrees and grounding the signal having the frequency F2 or F3 and becomes a high impedance with respect to the frequency component. As a result, isolation between the route side of the F2 or F3 transmission and reception system of F2 or F3TX and F2 or F3RX and the route side of the F1 or UMTS transmission and reception system can be secured. Due to this, the influence of the route side of the F1 or UMTS transmission and reception system upon the route side of the F2 or F3 transmission and reception system

can be reduced and, at the same time, the loss of the transmission route at the frequency F2 or F3 can be made small.

(2) Time when receiving receiver signal F2 or F3RX
5 using frequency F2 or F3

The controlling means 60 sets the switch element 21 to the opening (off) state, sets the switch element 23 or the switch element 24 to the closed (on) state, then sets the switch element 44 to the closed state. Naturally, the
10 controlling means 60 holds the switch element 32 and the switch element 33 in the opening state.

In this state, among the receiver signals F2 or F3RX applied to the antenna terminal 11 from the wireless transmitter and receiver antenna ANT, the F2 receiver
15 signal F2RX is output from the terminal 15 when the switch element 23 is in the closed state, and the F3 receiver signal F3RX is output from the terminal 16 when the switch element 24 is in the closed state.

The mode of operation of the 90 degree phase
20 rotation circuit 40 when the switch element 44 is set to the closed state is the same as the above description. Namely, it exhibits the characteristic that the circuit configured by the inductor 41 and the capacitors 42, 43 and 45 rotates the phase of the signal having the
25 frequency F2 or F3 by 90 degrees and is grounded and becomes a high impedance with respect to the frequency

component. As a result, isolation between the route side of the reception system of F2 or F3RX and the route side of the F1 or UMTS transmission and reception system can be secured. By this, the influence of the route side of the F1 or UMTS transmission and reception system upon the route side of the F2 or F3 reception system can be reduced and, at the same time, the loss of the transmission route at the frequency F2 or F3 can be made small.

10 (3) Time when transmitting transmitter signal F1TX using frequency F1

 The controlling means 60 sets the switch element 32 to the closed (on) state and sets the switch element 33 and switch element 44 to the opening (off) state.
15 Naturally, it holds the switch element 21 and the switch elements 23 and 24 in the opening state.

 The F1 transmitter signal F1TX applied to the F1 transmitter signal use terminal 13 passes through the switch element 32, is input to the diplexer 31 from the LPF side terminal 31b of the diplexer 31, passes through the LPF in the diplexer 31, passes through the 90 degree phase rotation circuit 40, is applied to the antenna terminal 11, and is transmitted from the wireless transmitter and receiver antenna ANT.

25 At this time, the 90 degree phase rotation circuit 40 functions as an LPF for attenuating the harmonic

components of the UMTS transmission frequency according to the attenuation characteristic exemplified in FIG. 4 and preventing it from passing, but becomes the pass band with respect to the transmission frequency F1 (for
5 example 900 MHz). Accordingly, the loss in this transmission route mainly becomes the loss in the diplexer 31. The loss of the diplexer 31 is extremely small, therefore it is possible to reduce the loss of the transmission route at the frequency F1.

10 (4) Time when receiving receiver signal F1RX using frequency F1

The controlling means 60 sets the switch element 33 to the closed (on) state and sets the switch element 32 and the switch element 44 to the opening (off) state.
15 Naturally, it holds the switch element 21 and the switch elements 23 and 24 in the opening state.

The receiver signal F1RX using the frequency F1 is input from the terminal 11 of the wireless transmitter and receiver antenna ANT, passes through the 90 degree
20 phase rotation circuit 40, is separated to the LPF side in the diplexer 31, and is output to the reception terminal 14 through the switch element 33 in the closed (on) state.

At this time as well, the 90 degree phase rotation
25 circuit 40 becomes the pass band with respect to the receiver signal having the frequency F1. The loss in this

reception route becomes loss in the diplexer 31,
therefore the loss of the reception route at the
frequency F1 can be reduced.

(5) Transmission of UMTS transmitter signal

5 The controlling means 60 sets all switch elements of
the switch element 21, the switch elements 23 and 24, the
switch element 44, and the switch elements 32 and 33 to
the opening (off) state.

 The UMTS transmitter signal is input from the
10 transmission and reception terminal 17, further input
from the HPF side terminal 31c of the diplexer 31, passes
through the HPF in the diplexer 31, then passes through
the 90 degree phase rotation circuit 40, is applied to
the antenna terminal 11, and is transmitted from the
15 wireless transmitter and receiver antenna ANT.

 The 90 degree phase rotation circuit 40 becomes the
pass band with respect to the UMTS transmission frequency
and becomes the attenuation band with respect to the
harmonics (frequencies 2 times or 3 times the UMTS
20 frequency) as illustrated in FIG. 4, thus can suppress
the harmonic components. The loss in this transmission
route mainly becomes the loss in the diplexer 31. Since
the loss of the diplexer 31 is extremely small, the loss
of the transmission route at the UMTS transmission
25 frequency can be reduced.

(6) Reception of UMTS receiver signal

The controlling means 60 sets all switch elements of the switch element 21, the switch elements 23 and 24, the switch element 44, and the switch elements 32 and 33 to the opening (off) state.

5 The UMTS receiver signal received at the wireless transmitter and receiver antenna ANT is applied to the antenna terminal 11, passes through the 90 degree phase rotation circuit 40, then is separated to the HPF side 31c by the diplexer 31, and output through the
10 transmission and reception terminal 17.

 The 90 degree phase rotation circuit 40 becomes the pass band with respect to the UMTS receiver signal. The loss in this reception route mainly becomes the loss in the diplexer 31, therefore the loss of the reception
15 route at the UMTS reception frequency can be reduced.

 As explained above, in the case of dual mode compatibility of the triple band GSM system and the UMTS system, at the time of signal route switching by the signal route switch 20 of the F2 or F3 transmission and
20 reception system, by preventing the frequency component on the signal route switch 20 side of the F2 or F3 transmission and reception system from being supplied to the signal route switching circuit 30 side of the F1 or UMTS transmission and reception system by the function of
25 the 90 degree phase rotation circuit 40, isolation between the route side of the F2 or F3 transmission and

reception system and the route side of the F1 or UMTS
transmission and reception system is secured and mutual
influence can be reduced, therefore the loss at the time
of the switching of the triple band and dual mode is
5 reduced, thus the circuit can be accomplished with a low
power consumption and small size circuit configuration.

Further, by imparting an attenuation characteristic
with respect to the harmonic components of the UMTS
frequency to the 90 degree phase rotation circuit 40 as
10 illustrated in FIG. 4, the harmonic components of the
UMTS frequency can be attenuated in the 90 degree phase
rotation circuit 40. As a result, in the UMTS use
transmission and reception circuit explained later by
referring to FIG. 5, when assuming that the passing loss
15 at the duplexer (band switching unit) for switching the
transmitter signal and the receiver signal is the same as
that when an attenuation characteristic is not imparted
to the 90 degree phase rotation circuit 40, the
configuration of the duplexer can be simplified by
20 exactly the amount able to attenuate the harmonic
components of the UMTS frequency. Conversely, when
assuming that the configuration of the duplexer is the
same as that when the attenuation characteristic is not
imparted to the 90 degree phase rotation circuit 40, the
25 passing loss with respect to the UMTS frequency can be
reduced by exactly the amount able to attenuate the

harmonic components.

In a mobile communication terminal such as a mobile phone (cellular phone) or PDA, there is a strong need for a smaller and lower power consumption in a multi-band, for example, composite terminal mounting a plurality of devices such as a mobile phone and digital camera. Especially, in the wireless signal switching circuit, as explained above, assuming use of one transmitter and receiver antenna, it is necessary to realize a multi-route switching circuit for switching multi-bands with a low loss, low power consumption, and small size. Contrary to this, in the wireless signal switching circuit 1 according to the present embodiment explained by referring to FIG. 2, isolation between the route side of the F2 or F3 transmission and reception system and the route side of the F1 or UMTS transmission and reception system is secured by using the 90 degree phase rotation circuit 40. Low loss in each route can be realized in each route by using not only the switch elements 21 to 24, 32 and 33, but also the diplexer 31 in order to separate the multi-routes. Accordingly, according to the present embodiment, a multi-route switching circuit for switching multi-bands is realized with a low loss, low power consumption, and small scale circuit configuration.

As explained by referring to FIG. 1, when employing a configuration separating routes by inserting a

plurality of switch elements in series in certain signal routes, losses in the switch elements accumulate and become a large loss. Further, when employing a configuration switching multi-routes by only switch
5 elements, when for example integrating a plurality of mutually connected switch elements, for example, field effect transistors FET (formed them into an IC), due to the parasitic capacitances in the plurality of switch elements connected to each other, there is a possibility
10 that the loss will increase by particularly the increase of switch elements in the off state (opening state). Contrary to this, in the wireless signal switching circuit 1 according to the present embodiment, by employing a configuration separating routes by using the
15 90 degree phase rotation circuit 40 and the diplexer 31, the number of switch elements inserted on the middle of the routes can be reduced and, at the same time, the number of switch elements connected to each other can be reduced, consequently the detrimental influence due to
20 the parasitic capacitances in the switch elements can be reduced and multiple routes can be separated with a low loss.

When realizing the wireless signal switching circuit of the present invention, the circuit configuration is
25 not limited to the circuit configuration of the 90 degree phase rotation circuit 40 exemplified in FIG. 2. In the

wireless signal switching circuit 1 of the present
embodiment, values of the components of the 90 degree
phase rotation circuit 40, that is the inductance L1 of
the inductor 41 and the capacitances C1 to C3 of the
5 capacitors 42, 43 and 45, were set so that the 90 degree
phase rotation circuit 40 rotates the phase of the
frequencies F2 and F3 by 90 degrees and functions as an
LPF with respect to the UMTS route frequency. In place of
this, the components of the 90 degree phase rotation
10 circuit 40 (any number of used capacitors and inductors
and circuit configuration) may be adjusted so that the 90
degree phase rotation circuit 40 becomes an LPF with
respect to the frequency F1 used in the LPF side route of
the diplexer 31. In this case, it must also be the pass
15 band with respect to the UMTS frequency.

It is also possible to make the 90 degree phase
rotation circuit 40 function as not only an LPF of the
diplexer 31, but also as a filter widely.

In the present embodiment, the case of using two
20 communication systems of the GSM system and the UMTS
system as different communication systems was explained
as an example, but communication systems other than these
communication systems may also be used. It is also
possible to apply the present invention to the case where
25 three or more different communication systems are used in
the same way as above.

The wireless signal switching circuit according to the present embodiment explained above is preferably used for switching a plurality of routes for a plurality of transmitter and receiver signals having different
5 frequencies in a multi-mode compatible mobile wireless communication apparatus such as a mobile phone or PDA.

Second Embodiment

FIG. 5 is a block diagram of an example of the partial configuration of a mobile wireless communication
10 apparatus using the wireless signal switching circuit of the present invention, for example, a dual mode compatible mobile phone of the triple band GSM system and the UMTS system, as an example of the wireless communication apparatus of the present invention.

15 As clear from the illustration of FIG. 5, the dual mode compatible mobile phone according to the second embodiment is configured provided with an antenna 51, a wireless signal switching circuit 52, transmission and reception circuits 53, 54 and 55 provided corresponding
20 to the frequencies F1, F2 and F3 of the GSM system, and a transmission and reception circuit 56 of the UMTS system.

The wireless signal switching circuit 52 is provided with an antenna terminal 521 connected to an antenna end 51A of the antenna 51, a reception terminal 522 of the
25 receiver signal having the frequency F1 and a transmission terminal 523 of the transmitter signal

having the frequency F1, a reception terminal 524 of the receiver signal having the frequency F2 and a transmission terminal 525 of the transmitter signal having the frequency F2, a reception terminal 526 of the receiver signal having the frequency F3 and a transmission terminal 527 of the transmitter signal having the frequency F3, and a UMTS transmission and reception terminal 528.

The front end of the UMTS transmission and reception circuit 56 is configured provided with a duplexer (band switching unit) 561 for switching the transmitter signal and the receiver signal, a low noise amplifier circuit 562 for amplifying the receiver signal input through this duplexer 561, and a power amplifier circuit 563 for amplifying the transmitter signal.

As the wireless signal switching circuit 52, use is made of the wireless signal switching circuit 1 explained above with reference to FIG. 2.

In the circuit configuration, in the correspondence with FIG. 2, the transmission terminal 13 of FIG. 2 corresponds to the transmission terminal 522, the reception terminal 14 corresponds to the reception terminal 523, the transmission terminal 12 corresponds to the transmission terminal 524 and the transmission terminal 526, the reception terminal 15 corresponds to the reception terminal 525, the reception terminal 16

corresponds to the reception terminal 527, and the transmission and reception terminal 17 corresponds to the transmission and reception terminal 528.

The transmission and reception circuit 53 for the
5 frequency F1 is connected to the transmission terminal 522 and the reception terminal 523, the transmission and reception circuit 54 for the frequency F2 is connected to the transmission terminal 524 and the reception terminal 525, and the transmission and reception circuit 55 for
10 the frequency F3 is connected to the transmission terminal 526 and the reception terminal 527. Further, the UMTS transmission and reception circuit 56 is connected to the transmission and reception terminal 528.

As previously explained, the wireless signal
15 switching circuit 1 explained with reference to FIG. 2 can realize the switching of the triple band and the dual mode with a low loss, low power consumption, and small scale circuit configuration. Accordingly, the use of the wireless signal switching circuit 1 in the mobile phone
20 partially illustrated in FIG. 5 can greatly contribute to the lower power consumption, smaller size, and lighter weight of the mobile phone.

Especially, the 90 degree phase rotation circuit 40 illustrated in FIG. 2 has an attenuation characteristic
25 with respect to the harmonic components of the UMTS frequency and can attenuate the harmonic components of

the UMTS frequency. For this reason, in the UMTS transmission and reception circuit 56, when assuming that the passing loss at the duplexer 561 is the same as that when the attenuation characteristic is not imparted to the 90 degree phase rotation circuit 40, the configuration of the duplexer 561 can be simplified by exactly the amount able to attenuate the harmonic components of the UMTS frequency. Conversely, when assuming that the configuration of the duplexer 561 is the same as that when the attenuation characteristic is not imparted to the 90 degree phase rotation circuit 40, the passing loss with respect to the UMTS frequency can be reduced by exactly the amount able to attenuate the harmonic components.

As explained above, according to the wireless signal switching circuit of the present invention, the route switching is carried out by using not only switch elements, but also a duplexer while securing isolation between the transmission route and the reception route by using a 90 degree phase rotating means, therefore a multi-route switching circuit for switching multi-bands can be realized by a low loss, low power consumption, and small scale circuit configuration.

Further, according to the present invention, by using such a wireless signal switching circuit in a wireless communication apparatus, the wireless

communication apparatus can be configured low in power consumption, small in size, and light in weight.

Further, according to the present invention, by imparting an attenuation characteristic with respect to
5 the harmonic components of the UMTS frequency by a 90 degree phase rotating means, the harmonic components of the UMTS frequency can be attenuated. For this reason, the passing loss with respect to the frequency of UMTS can be reduced.

10 The present invention is not limited to the above illustration. Various modifications can be employed based on the technical idea of the present invention. Accordingly, the present invention covers the invention disclosed in the claims and a scope equivalent to the
15 invention.